



Attorney's Docket No.: 07977-052001 / US3053

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani et al Art Unit : 1765
Serial No.: 08/690,747 Examiner : Robert Kunemund
Filed : August 1, 1996
Title : METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

COPY OF PAPERS
ORIGINALLY FILED

Commissioner for Patents
Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.

The required fees are computed below. Please apply any charges

not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	62	-	62	=	0	\$0
Independent	13	-	13	=	0	\$0
First Presentation of Multiple Dependent						\$0
Claims						

Applicant hereby petitions under 37 C.F.R.
§1.136 for a 2 month extension of time.

\$400
\$400

TOTAL FEE DUE

A check for \$400 is attached.

RECEIVED

FEB 19 2002

TC 1700

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Feb 19 2002

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[Signature]

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Certificate

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Respectfully submitted,

Donald C. Kordich
Reg. No. 38,213

Date: 16/17/2001

For Scott C. Harris
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